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35617 7590 06/17/2004

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September 9, 2004

Date

Kevin L. Daffer

APPLICATION NO.	FILING DATE	FIRST NAME(S) INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/849,047	05/04/2001	Gabriel Li	5298-04500	9067

TITLE OF INVENTION: BURIED LAYER SUBSTRATE ISOLATION IN INTEGRATED CIRCUITS

APPL. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEES DUE	DATE DUE
nonprovisional	NO	\$1,330.00	\$0.00	\$1,330.00	09/17/2004
EXAMINER	ART UNIT	CLASS SURCLASS			
Lewis, Monica	2822	257-509.000			

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1 Kevin L. Daffer

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(A) NAME OF ASSIGNEE: Cypress Semiconductor Corp.

(B) RESIDENCE (CITY & STATE OR COUNTRY): San Jose, CA

Please check the appropriate assignee category indicated below (will not be printed on the patent): ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature) 	Kevin L. Daffer Reg. No. 34,146	(Date) 9/9/04
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09/10/2004 HAL122 00000025 032769 09849047

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